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Total Number of Pages in This Submission

Application Number 10/708,478 Filing Date March 5, 2004 First Named Inventor White, Thomas H. Art Unit 2818 Examiner Name Chang, Daniel D. Attorney Docket Number 015114-070200US

ENCLOSURES (Check all that apply)								
	Fee Trans	mittal Form		Drawing(s)			After Allowance Communication to TC	
	Fe	ee Attached		Licensing-related Paper	rs		Appeal Communication to Board of Appeals and Interferences	
	Extension Express A	ent/Reply iter Final fidavits/declaration(s) of Time Request bandonment Request		Terminal Disclaimer Request for Refund	Application ttorney, Revocation Correspondence Address isclaimer r Refund		Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosure(s) (please identify below): n Postcard, Response to Restriction and on of Claims	
[] Inioiniation disclosure		ii Disclosure Statement	CD, Number of CD(s)					
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT								
Firm Name		Townsend and Townsend and Crew LLP						
Signature		Bulale						
Printed name		Babak S. Sani						
Date		September 14, 2005			Reg. No.	37,49	95	

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TOWNSEND and TOWNSEND and CREW

By: Offmelonk

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

WHITE, Thomas H. et al.

Application No.: 10/708,478

Filed: March 5, 2004

For: TECHNIQUES FOR

CONFIGURING PROGRAMMABLE

LOGIC USING ON-CHIP NONVOLATILE MEMORY Examiner:

CHANG, Daniel D.

Art Unit:

2477

RESPONSE TO RESTRICTION AND ELECTION OF CLAIMS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement mailed September 2, 2005, applicant elects with traverse to prosecute the claims of Group II (1-11 and 24-33).

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 415-576-0200.

Respectfully submitted,

Babak S. Sani Reg. No. 37,495

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